

Resonant-Repetitive Combined Control for Stand-Alone Power Supply Units

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Abstract—This paper investigates a combined Resonant-Repetitive (RR) control structure for a three phase four leg DC/AC converter power supply. The RR control configuration is composed by a resonant controller tuned at the system fundamental frequency working in conjunction with a plug-in type repetitive controller. The resonant part of the control scheme is used to assure prompt tracking of the inverter output voltage as well to achieve as fast as possible system response to load variations; to this purpose, it is tuned at the fundamental frequency. At the same time, the resonant controller is able to stabilize the system without the necessity of any further additional controller; the repetitive part of the scheme is implemented for the fine regulation at the system harmonic frequencies. The proposed control configuration is used to regulate the power supply output voltage providing very good tracking of the output voltage reference even in the presence of a non-linear load. Experimental validation from a 40 kVA converter prototype is presented to validate the operation of the proposed converter and control.

Index Terms – Distributed power generation, DC-AC power converters, harmonic distortion, repetitive control, resonant control, unbalanced loads.

I. INTRODUCTION

Distributed generation is being developed worldwide to meet the increasing energy demand as well to reduce the environmental impact of fossil-fuel based centralized generation. Conventional electric power plants have rated power of thousands MVA and are usually located far from the load areas where the energy supply is needed. On the contrary, distributed generation systems are accomplished by using renewable energy generating units (e.g., photovoltaic arrays and wind turbines) which have relatively low power rating and are suitably connected to the power grid at the voltage level of the electricity distribution network, so that the significant investment in electric transmission infrastructure can be substantially reduced. However, due to the intermittent nature of the renewable energy sources and the desire for generating operation with maximum power point tracking, energy storage is usually needed and thereby the various electric power sources are operated in parallel against a common DC link by means of power electronic converters, as schematically shown in Fig. 1.

Hence, a DC-AC converter with 4-wire output is used for connecting the generating system to the distribution network

and the user loads. Such a converter is required to provide both active and reactive power output depending on whether it is operated on-grid or off-grid. The off-grid or even stand-alone mode of operation is the most severe in terms of control stability as well as waveforms total harmonic distortion (THD); in fact, the absence of the grid connection, which is known as low impedance connection, makes the whole generating system easily affected by non-linear 3-phase and 1-phase loads. As a consequence, the paper refers to the off-grid mode of operation of the DC-AC converter with 4-wire output in order to investigate, in particular, the control issues.

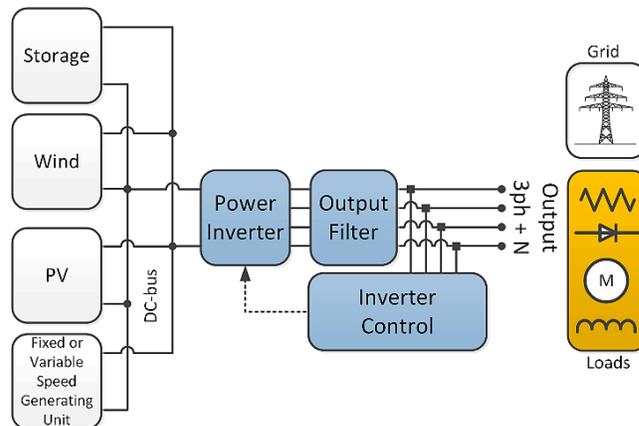


Fig. 1. Example of Distributed Generating System.

The 3-phase 4-leg inverter topologies make use of an additional leg with respect to the conventional 3-phase topologies as it is shown in Fig. 2. The additional leg requires 2 more switches and power diodes as well extra driving circuits and higher complexity in modulation techniques and control strategies. In spite of this, the 4-leg inverter, when properly modulated, can assure the efficient regulation of the output phase voltage also in case of unbalanced and distorted loads. Injection of 3rd harmonic as well SVM techniques are possible without affecting the harmonic content in phase-to-neutral voltage [1]; further, the neutral current flows through the added leg and no oversizing is required for the DC-link capacitors.

Emerging control techniques, in the field of power electronics for both on-grid and off-grid electric generating systems, are the resonant control (RSC) and the repetitive control (RC) which have found significant space in the last

decade literature. Both RC and RSC are based on the Internal Model Principle. RSC is directly able to compensate any sinusoidal disturbance even if not multiple of the fundamental harmonic. More details are shown in Section II. RC uses the error signal of previous cycle to improve the performance of current cycle and it is described in details in Section III.

In practical applications, the control system can become unstable where high order RSCs are used to compensate the harmonic distortion introduced by the loads. It causes the reduction of the overall control accuracy due to the limited number of harmonic controllers that can be used. This behavior is strongly related to the output power filter phase lagging which heavily depends on the load condition. At the same time, the repetitive control is not able to ensure system stability by itself and it needs an additional control action. Also with RC the phase behavior of the whole system is of critical importance.

Some solutions for resonant controller phase control have been shown in literature [2], where the resonant controller ideal transfer function is extended including phase compensation. However, the proposed control structure does not consider the influence of the load variation in the tuning process and during the inverter operation. Phase control in RC has been preliminary investigated in [3].

The paper first discusses both the RSC and the RC basic characteristics and then the combination of the two control structures (RR) is investigated with reference to a 4-leg DC-AC converter for power supply of unbalanced and non-linear loads.

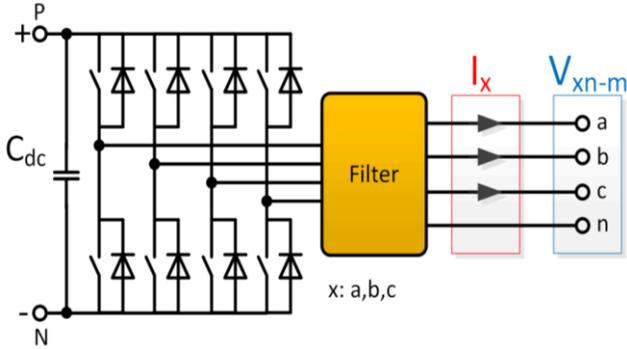


Fig. 2. 3-Phase 4-Leg Inverter. x: a, b, c line. n: neutral. m: measured.

II. RESONANT CONTROL

Resonant controllers can be synthesized through different analytical expressions, the ideal form, the approximated form and the full real form. Each RSC can be combined with others to form the so-called Multi Resonant Control Structure [4], where several harmonics can be compensated.

The RSC ideal form is very simple and it is shown in (1)

$$G_{RSCi}(s) = \frac{k_{ir}s}{s^2 + \omega_0^2} \quad (1)$$

where k_{ir} is the controller gain and ω_0 the resonance frequency. However, the ideal form should be avoided due to its infinite gain at the resonance frequency, which makes the transfer function difficult to be digitally implemented on industrial grade Digital Signal Processors (DSPs) working with fixed or single precision floating-point arithmetic. Moreover, it does not allow selecting the appropriate controller gain and width, resulting in a strong reduction of control design flexibility.

An alternative RSC form can be achieved by the approximated full-structure expressed as in (2)

$$G_{RSCa}(s) = \frac{2k_{ir}\omega_{cr}s}{s^2 + 2\omega_{cr}s + \omega_0^2} \quad (2)$$

where ω_{cr} is the controller width. However, the validity of the approximation, $\omega_{cr} \ll \omega_0$, must be verified every time the controller gains are changed.

In order to provide a more general RSC form, the full real resonant controller having the transfer function reported in (3) can be considered.

$$G_{RSCf}(s) = \frac{2k_{ir}(\omega_{cr}s + \omega_{cr}^2)}{s^2 + 2\omega_{cr}s + \omega_{cr}^2 + \omega_0^2} \quad (3)$$

As depicted in [5] RSC allows also regulating the controller phase at the resonant frequency, increasing the design flexibility with three independent degrees of freedom: controller gain, width and phase (4).

$$G_{RSCfg}(s) = \frac{2k_{ir}\omega_{cr}(s \cos(\vartheta) + \omega_{cr} - \omega_0 \sin(\vartheta))}{s^2 + 2\omega_{cr}s + (\omega_{cr}^2 + \omega_0^2)} \quad (4)$$

Each RSC can be part of a more complex structure when more than one RSC are combined to provide compensation at different frequencies. Moreover, the RSC requires the addition of an integral term to the whole transfer function in order to effectively regulate the DC component. As in more conventional control structures, a proportional gain can be inserted in the transfer function to increase the dynamic behavior. The general control scheme related to the aforementioned control topologies is shown in Fig. 3a where a multi resonant structure can include both proportional gain K_p and integral action K_i/s ; whereas, Fig. 3b depicts the bode plots for the previously described RSC based control alternatives. Resonant controller design can be carried out using well-known techniques as in [6]. However, the maximum harmonic order in the multi-resonant control structure is limited by the load-filter interaction, which causes the decreasing of resonant controllers' magnitude and phase. In particular, the phase variation causes the reduction of the system phase margin and proper compensation strategies must be implemented in the control algorithm [5].

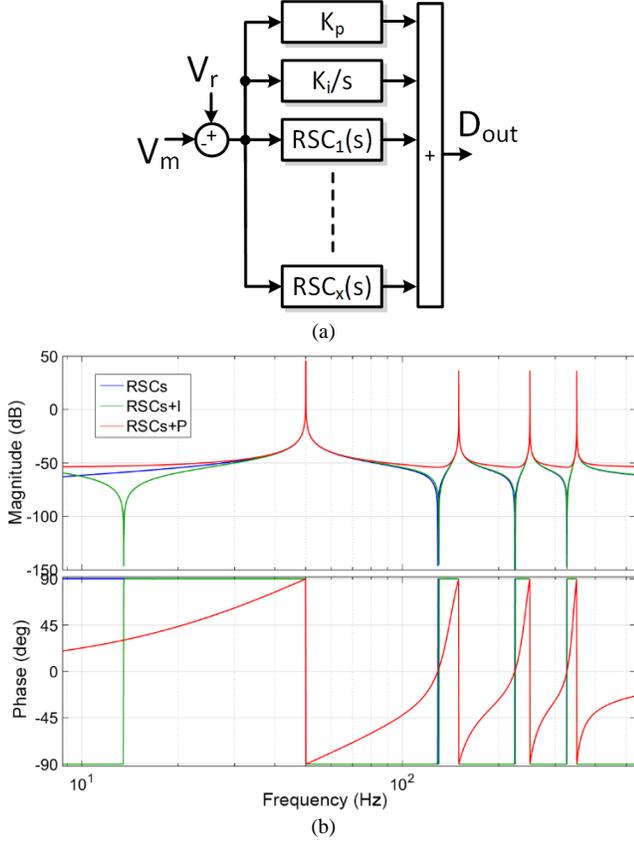


Fig. 3. a) Block scheme of different RSC based control structures. V_r : reference. V_m : measure. D_{out} : control signal forwarded to the modulator, b) Bode plots for different RSC based control topologies.

III. REPETITIVE CONTROL

Repetitive Control represents an optimal control solution for feedback systems that are subject to periodic inputs [7-11]. Differing from traditional approaches, a repetitive controller is not intended to stabilize the plant, but to augment an existing control system for removal of the periodic error signals. Based on the Internal Model Principle (IMP) [12], the repetitive controller processes the error signal of the previous period and applies the resultant signal to improve the control performance of the current cycle. Theoretically, with a suitably designed RC, the output of a stable feedback system can track the periodic reference signal or/and reject the exogenous periodic disturbance with zero steady state error even in the presence of model uncertainties. Based on the configuration with which they are integrated with the pre-existent stabilizing control system, repetitive controllers can be roughly divided into three main categories: direct type, parallel type and plug-in type. Fig. 4 illustrates the three configurations, where r , e , y , G_P , G_C and G_{RC} represent the periodic reference, the error signal, the system output, the plant, the stabilizing controller (which will be the resonant controller at 50 Hz in this work) and the repetitive controller, respectively.

In this work, the selected plug-in type RC form allows to design G_C individually to stabilize the target plant G_P , which is particularly helpful for internally unstable and non-minimum phase systems. Fig. 5 shows the plug-in type RC feedback control system used in this paper, where the structure of the RC is highlighted in detail. K_{RC} is the repetitive learning gain, z^{-M} is the delay line, $Q(z)$ is the robustness filter, and $G_f(z)$ is the stability filter. M is the ratio between the pulse period T_p and the sampling time T_s .

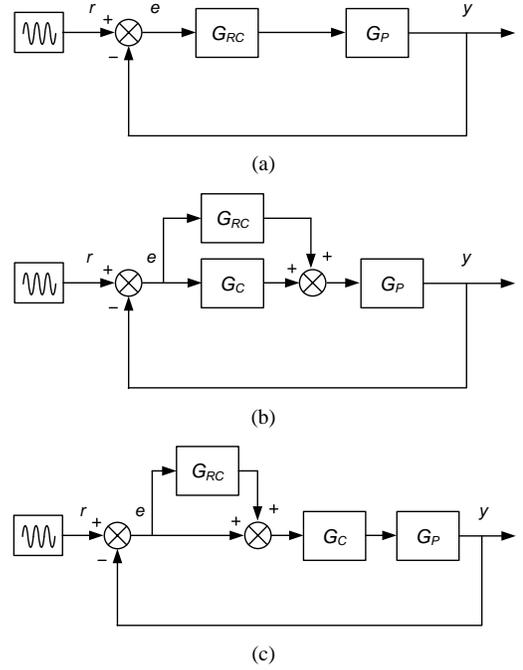


Fig. 4. Structures of Repetitive Control Systems. a) direct type, b) parallel type, c) plug-in type.

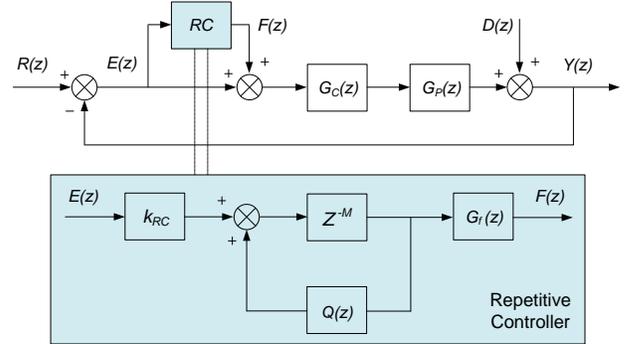


Fig 5. Plug-in Type RC control system.

The RC transfer function is presented in (5).

$$RC = \frac{F(z)}{E(z)} = \frac{k_{RC} G_f(z) z^{-M}}{1 - Q(z)z^{-M}} \quad (5)$$

Taking RC into account and including the effect of periodic disturbances, the error transfer function for the overall system is expressed in (6) [8].

$$E(z) = \frac{R(z) - D(z)}{1 + (1 + RC)G_c(z)G_p(z)} \quad (6)$$

$$= \frac{(R(z) - D(z)) \cdot (1 - Q(z)Z^{-M})}{(1 + G_c(z)G_p(z)) \cdot [1 - Z^{-M} (Q(z) - \frac{k_{RC} G_f(z)G_c(z)G_p(z)}{1 + G_c(z)G_p(z)})]}$$

Based on the small gain theorem [9], two sufficient stability conditions for the plug-in RC system are derived as follows: first, the roots of $1 + G_c(z)G_p(z)$ need to remain within the unit circle; second, equation (7) is guaranteed for all frequencies below the Nyquist frequency.

$$\left| S(e^{j\omega T_s}) \right| = \left| Q(e^{j\omega T_s}) - \frac{k_{RC} G_f(e^{j\omega T_s})G_c(e^{j\omega T_s})G_p(e^{j\omega T_s})}{1 + G_c(e^{j\omega T_s})G_p(e^{j\omega T_s})} \right| < 1 \quad (7)$$

IV. RESONANT-REPETITIVE COMBINED CONTROL

In this work, a resonant-repetitive combined control strategy is considered for the target 3-phase 4-leg inverter system. The RSC is designed and tuned at the main frequency of 50 Hz, which ensures not only sufficient stability margin for the system, but also a fast and smooth output dynamics before RC takes action. The aim of RC is to compensate higher order of harmonics cycle by cycle, and eventually achieve zero steady state error.

Complete system modeling has been accomplished according to the block scheme shown in Fig. 6 where the plant transfer function $G_p(s)$ is composed respectively by the model of both the 4-leg VSI, $G_{4-leg}(s)$, and the output power filter, $G_{pwf}(s)$. $G_{ipf}(s)$ takes into account the measurement chain as an equivalent Butterworth type II order low-pass filter having a cut-off frequency of 10 kHz. These subsystems have been modeled according to the procedure shown in [6], with reference to the inverter output filter structure depicted in Fig. 7 and having the component parameters as listed in Table I.

From [6], complete system modeling equations are briefly reported as it follows

$$G_{4-leg}(s) = \frac{K_m V_{dc}}{1 + \frac{s}{2\pi F_{sw}}} \quad (8)$$

$$G_{ipf}(s) = \left(\frac{\omega_f^2}{s^2 + \sqrt{2}\omega_f s + \omega_f^2} \right) \quad (9)$$

$$Z_{trap}(s) = sL_t + \frac{1}{sC_t}, \quad Z_{dump}(s) = sL_d + \frac{1}{sC_d} + R_d \quad (10)$$

$$G_{pwf}(s) = \frac{Z_{dump}(s) // Z_{trap}(s) // \frac{1}{sC}}{sL_k + \left(Z_{dump}(s) // Z_{trap}(s) // \frac{1}{sC} \right)} \quad (11)$$

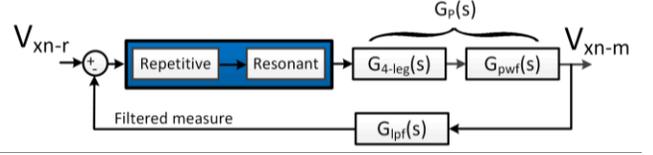


Fig. 6. Cascade configuration for the proposed RR control. x: a,b,c. r: reference voltage. m: measured output voltage. n: neutral voltage.

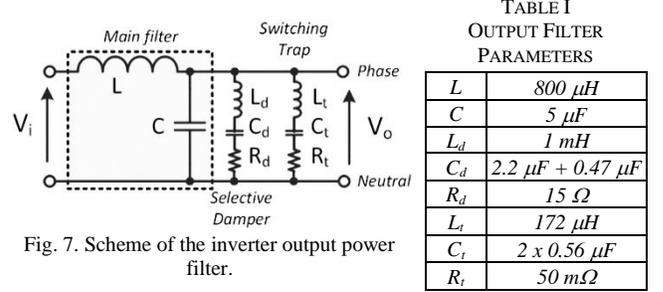


Fig. 7. Scheme of the inverter output power filter.

TABLE I
OUTPUT FILTER
PARAMETERS

L	800 μ H
C	5 μ F
L_d	1 mH
C_d	2.2 μ F + 0.47 μ F
R_d	15 Ω
L_t	172 μ H
C_t	2 x 0.56 μ F
R_t	50 m Ω

where K_m is the gain related to the modulation strategy, V_{dc} and F_{sw} are respectively the input DC-link voltage and the inverter switching frequency being assumed equal to the sampling frequency. With reference to $G_{ipf}(f)$, ω_f is the filter cut-off frequency. The output power filter transfer function (11) can be straightforwardly obtained from Fig. 8 using (10) yielding directly the expression for $G_{pwf}(s)$.

The filter architecture is investigated in [13] and it is intended to be used mainly in applications for off-grid operations. The LC_{main} section of the filter is a standard second order passive low-pass filter, composed by a power inductor and a high current ripple film capacitor. This basic topology is combined with one or more either LC or RLC tuned circuits, placed in parallel with the main capacitor C_f . In the considered filter structure, the main capacitor value has been strongly reduced with respect to conventional LC filters [14], improving the filter impedance at fundamental frequency and dramatically reducing the reactive current component.

With reference to Fig. 6, from Fig. 5 and (3), in the s-domain Resonant and Repetitive controllers can be combined as follows

$$G_{RR}(s) = G_{RC}(s)G_{RSC}(s) = \underbrace{\left(1 + \frac{K_{RC} e^{-T_1 s}}{1 - Q e^{-T_0 s}} \right)}_{\text{Repetitive}} \underbrace{\frac{2k_{ir}(\omega_{cr} s + \omega_{cr}^2)}{s^2 + 2\omega_{cr} s + \omega_{cr}^2 + \omega_0^2}}_{\text{Resonant}} \quad (12)$$

where T_0 is the main delay and T_1 is the delay related to the stability filter. The repetitive Control transfer function shown in (12) can be derived directly from the block scheme reported in Fig. 5, whereas a full Resonant Controller representation in s-domain can be written as shown in [5]. The controller parameters are tuned by means of Nyquist plot, however Bode plots or Nychols chart can be used as well. However, due to the well-known issues

arising during the resonant controller discretization process, a separate z-domain representation is considered. According to that, the RSC has been discretized applying the bilinear transformation with frequency pre-warp, whereas the RC has been implemented by the direct delay-line expression as in (5). The achieved combined controller form in discrete domain is compact and it can be straightforwardly implemented on industrial grade DSP.

All the controller parameters are summarized in Table II.

TABLE II
PARAMETERS OF THE PROPOSED RR CONTROL SYSTEM

Symbol	Description	Value
k_{rr}	Resonant controller gain	350
ω_{cr}	Resonant controller width	0.002
ω_0	Resonant controller frequency	314
K_{RC}	Repetitive controller gain	0.5
$Q(z)$	Robustness filter	0.98
$G_r(z)$	Stability filter	z^4
M	Delay line	240
f_s	Sampling frequency	12 kHz

Fig. 8 shows the Bode plot of the complete RR control system. Clearly, at the fundamental frequency and each harmonic frequency, the system has very high magnitude gain and phase shift of zero degree. This important feature implies that perfect reference tracking and/or disturbance rejection can be achieved in steady state at these frequencies. Fig. 9 depicts the locus curve of $S(e^{j\omega T_s})$ with $G_r(z)=z^4$, $Q(z)=0.98$, and $K_{RC}=0.5$ while the frequency varies. As it can be seen, the magnitude of the term $S(e^{j\omega T_s})$ stays within the unity circle centered at (0, 0) for the entire frequency range up to the Nyquist Frequency ω_{nyq} , so that the proposed RR system is proved to be stable.

The resulting Bode plot, for the complete system including RR control, VSI and output filter, is shown in Fig. 10, where the high gain provided by the combined control structure can be identified at the fundamental frequency. Lower and decreasing gains are related to the Repetitive Controller filtered by the right side band of the Resonant Controller, which therefore also substitutes the effect of the robustness filter $Q(z)$ chosen as a constant in our design. It can be noticed that around 1.7 kHz harmonics gain moderately increases due to the resonance of the output power filter.

In the proposed plug-in structure, the RSC component is used to assure prompt tracking of the inverter output voltage as well to achieve as fast as possible system response to load variations; to this purpose, it is tuned at the fundamental frequency. RSC acts as a low-pass filter when combined with the RC. It means that high frequency gains that are typical of the RC are reduced by the RSC without the need of an additional stabilizing output low-pass filter.

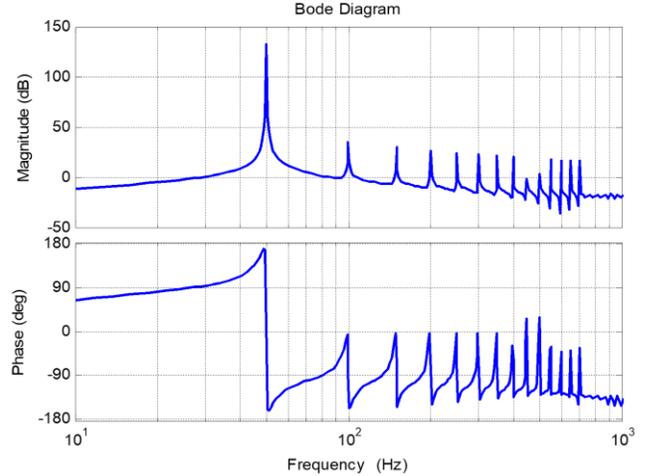


Fig. 8. Bode plot of the complete RR control system.

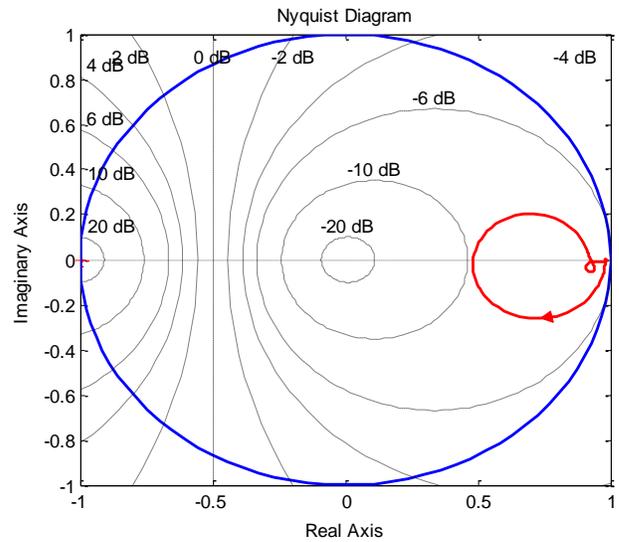


Fig. 9. Nyquist locus curve of $S(e^{j\omega T_s})$ with $G_r(z)=z^4$, $Q(z)=0.98$ and $K_{RC}=0.5$.

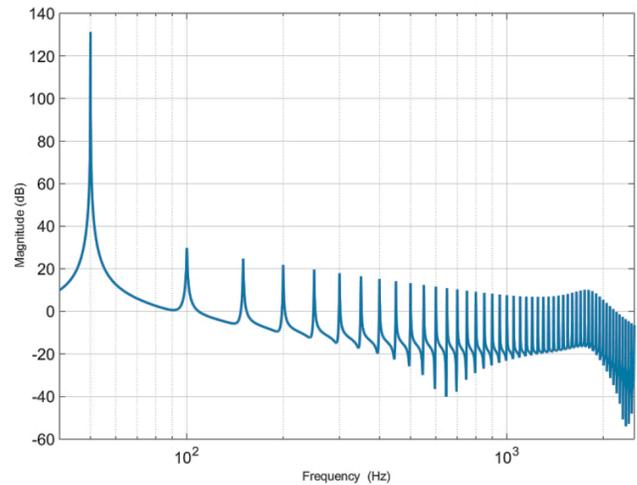


Fig. 10. Open-loop Bode plot for the complete system including RR control, VSI and output power filter.

Repetitive-Resonant control and its main competitor Multi-Resonant structure show similar control performances being both based on the Internal Model Principle, however, some differences can be highlighted. As opposed to a fully multi-resonant control solution, the proposed resonant-repetitive combined control strategy presents less stability issues, it is more robust to system parameters drift, it can compensate in a more efficient way the high order harmonics, as well its implementation on digital control platform is much easier than multi-RSC structure.

RR performances in harmonic compensation are limited by the required high-frequency attenuation to be provided to guarantee system stability. Multi-RSC shows a different implementation being based on a parallel tank of single resonant controllers. In industrial grade control platforms, the implementation of such a number of RSCs might oblige to increase the control algorithm sampling time due to the sequential execution of a large number of mathematical operations. Increasing the the sampling time, forces also to limit the number of RSCs that can be included in the Multi-RSC structure, resulting in reduced harmonic compensation capability. In conclusion, RR strategy allows a simpler implementation with strongly reduced computational burden and lowering the processor requirements, resulting in a reduced cost of the dedicated control platform, as it is described in the last Section of the paper.

A. Considerations on Resonant-Repetitive and Repetitive-Integral controls

Repetitive Control structures can be indifferently stabilized by an integral controller or, as previously mentioned, by a RSC at the fundamental frequency. However, Fig. 11a and Fig. 11b depict the different behavior at low frequency for the resulting controllers with same high frequency performance. The RR control shows higher gain in the neighborhood of the fundamental frequency with respect to the RC plus integral structure. When the comparison is made considering the same high frequency attenuation, the low gain exhibited by the Resonant-Integral (RI) controller at the fundamental frequency limits the tracking behavior with respect to reference variations, which can occur as an example during the VSI start-up procedure. Accordingly, in order to increase the gain at low frequencies, the integral action must be enhanced by increasing its gain. It results, of course, also in the amplifications of the high frequency resonances characteristics of the RC, thus reducing the stability margins.

When Repetitive Controller is combined with the resonant control structure, the resulting configuration allows more tuning flexibility. It provides the capability to regulate independently both the total gain at the fundamental frequency, and the required attenuation at high frequencies, by changing the RSC gain (k_{ir}) and width (ω_{cr}).

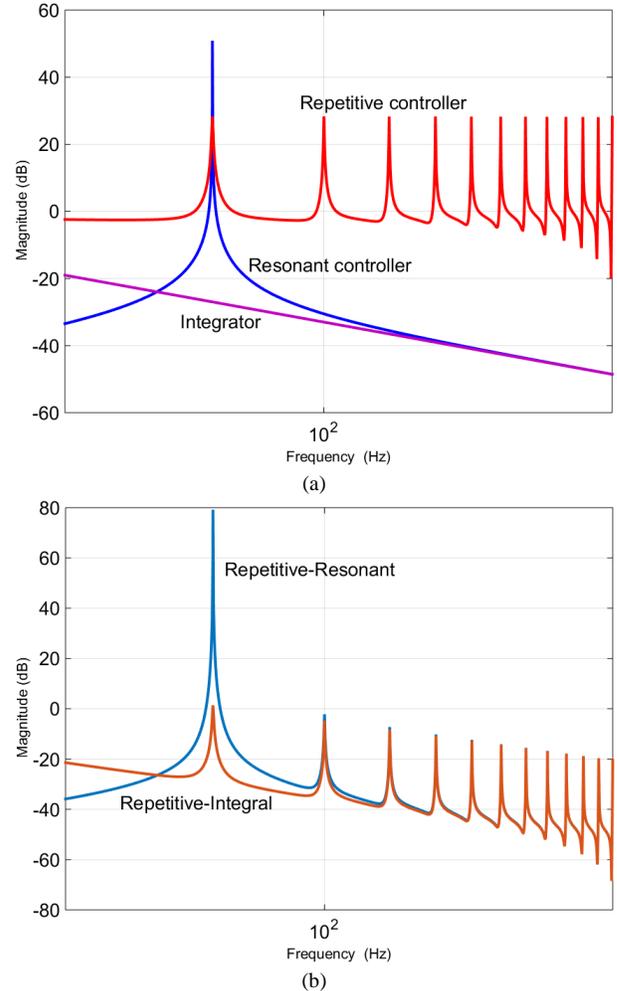


Fig. 11. Bode plot of the Resonant, Repetitive and Integral controllers. a) Repetitive, Resonant and Integral controllers, b) Repetitive-Resonant and Repetitive-Integral.

In addition, the proposed RR structure does not significantly increase the computational weight being required for the implementation, as it results from the investigation in Section VIII.

RR control is now compared to the RI solution; for this reason, two values of the integral gain are selected. The first one (RI1) is conceived in order to provide the same filtering action of the RSC, as it is shown in Fig. 11a; the second tuned RI (RI2) exhibits a higher integral gain than RI1 at the fundamental frequency. This second solution has been motivated by the need to improve the poor tracking performance of RI1 at fundamental frequency. Fig. 12a depicts the previously described behavior showing magnitude and phase Bode plots in the neighborhood of the fundamental frequency. It can be noticed how the RR provides better tracking capability and selectivity with respect to the RI1 and RI2 solutions. Further information can be obtained analysing the Bode plots at higher frequencies, as it is shown in Fig. 12b. The use of RSC or

Integral action does not affect the resulting phase behavior, as it is depicted in Fig. 12b; however, important differences are present in the magnitude of the analyzed controller solutions. The RI2 configuration is clearly not stable at frequencies even lower than 1 kHz. The RI1 controller is almost at the boundary of the stability condition with a very small gain margin. The proposed RR controller provides higher stability margins than the previously mentioned solutions. Jointly with the better tracking capabilities at the fundamental frequency, it offers also harmonic compensations and control chain stabilization.

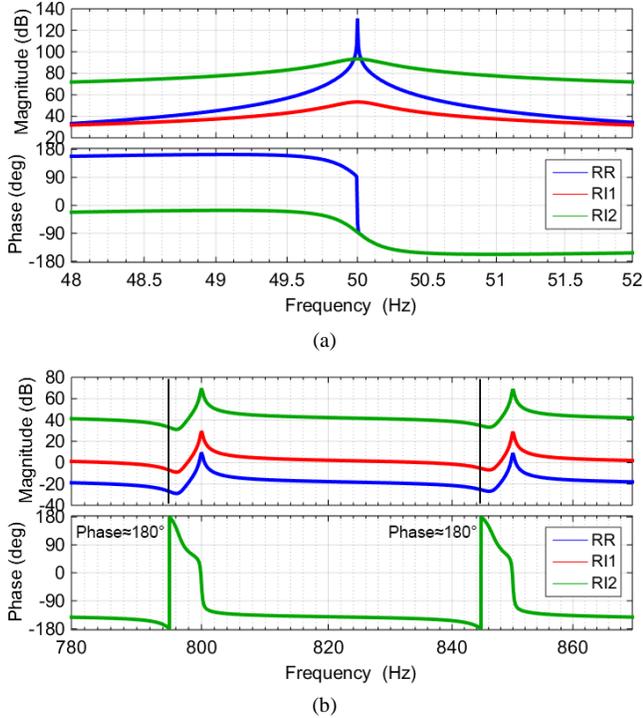


Fig. 12. Open-loop Bode plots of the complete system. a) centered across the fundamental harmonic, b) zoom across higher order harmonics.

V. SIMULATION RESULTS

First simulation results for the RR control structure have been obtained through the linearized model, in which the switching behavior is neglected. In this case Fig. 13 depicts both RC and combined RR control signals when no harmonic content affects the 230 V_{rms} phase-to-neutral output voltage; Fig. 14 shows the same control signals in case of output voltage harmonic content of 11%, 10% and 9% respectively for 3rd, 5th and 7th harmonic order.

The proposed RR configuration is capable of sharing the control effort between RC and RSC. In fact, RSC is devoted to provide tracking of the fundamental harmonic, whereas RC is used to cancel the output voltages harmonic contents introduced by distortive loads.

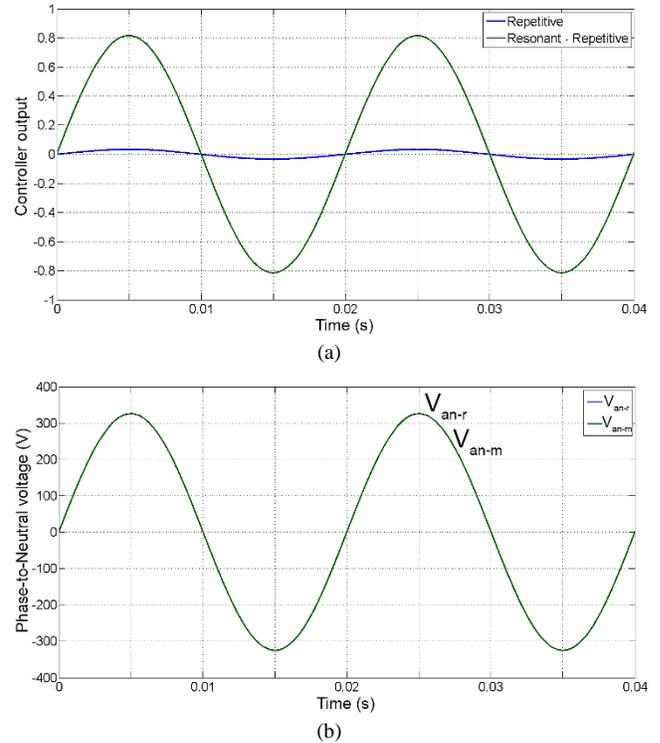


Fig. 13. Simulation Results with no harmonic content. a) RC and RR Signals, b) Phase-to-Neutral Output Voltage.

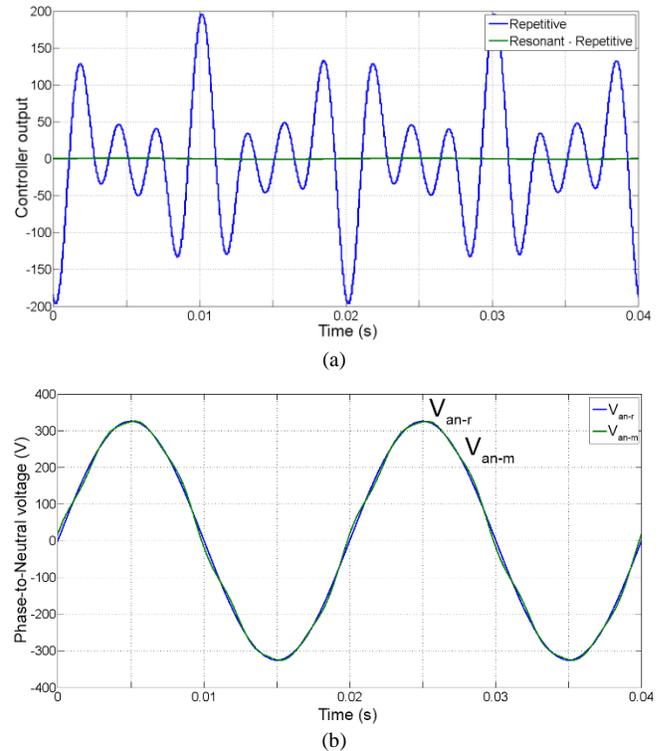


Fig. 14. Simulation Results with harmonic content. a) RC and RR Signals, b) Phase-to-Neutral Output Voltage.

Comprehensive analysis has been performed through a full switching model, which includes the single-precision floating-point 32-bit arithmetic, as in the selected Digital Signal Processor (DSP), synchronized sampling and PWM unit. The IGBT Dead-Time is equal to $3.3 \mu\text{s}$, and the modulation scheme is selected to be the Carrier based PWM with triangular third harmonic injection. The proposed RR control is designed to have high performance in output voltage regulation for both highly non-linear loads and heavy load variations. In order to highlight the behavior of the RR control, in the case of highly non-linear loads, test results are obtained when a three-phase diode rectifier load is fed by the 4leg VSI, as it is depicted in Fig. 15 and Fig. 16.

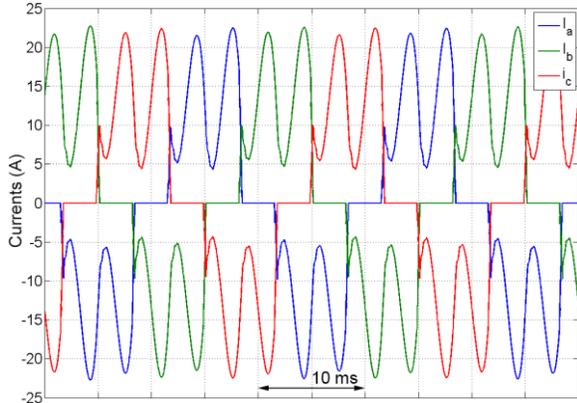


Fig. 15. Inverter output currents for the 3ph diode rectifier load.

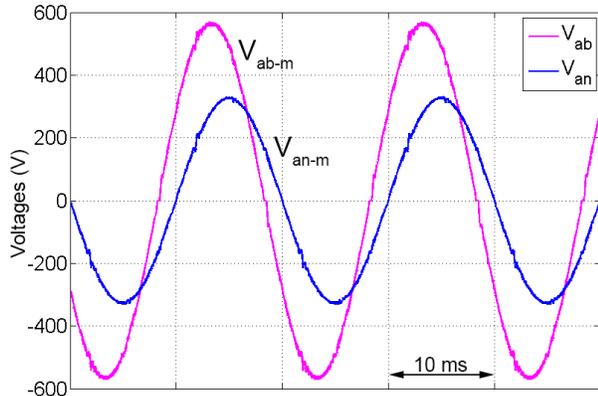


Fig. 16. Inverter output line-to-line voltage and phase voltage for the 3ph diode rectifier load with RR control structure fully enabled.

The non-linear load consists of a three-phase diode bridge rectifier configuration having 0.8 mH as input line inductance, 1.5 mF as output DC capacitance and 25Ω as DC load. Fig. 15 depicts the heavily distorted load currents. However, thanks to the effect of the RR algorithm, inverter output line-to-line voltage and phase voltage exhibit a quite low harmonic distortion as shown in Fig. 16, with a resulting THD around 1.7%. A similar simulation test is performed when the inverter is loaded by a single-phase diode rectifier, as it is shown in Fig. 17 and Fig. 18

respectively for the output currents and phase voltages. The sinusoidal output voltage shaping performed by the RR control structure provides a very low THD even in case of highly distorted and unbalanced currents.

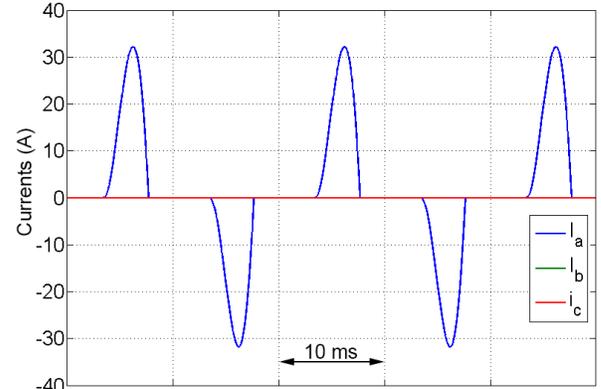


Fig. 17. Inverter output currents for the 1ph diode rectifier load

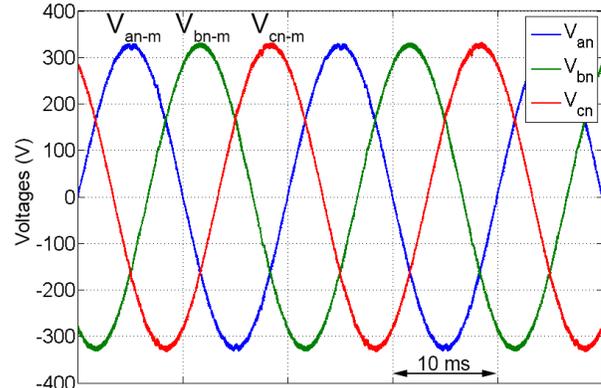


Fig. 18. Inverter output phase voltages for the 1ph diode rectifier load with RR control structure fully enabled.

VI. EXPERIMENTAL TESTS

Experimental tests have been performed to validate the proposed Resonant-Repetitive combined control architecture with parameters' values as in Table II.

Prototypal realization of a 3-phase 4-leg inverter as shown in Fig. 19 is used to experimentally investigate the resonant-repetitive combined control. The prototype is rated 40 kVA at $50 \text{ }^\circ\text{C}$ ambient temperature and its characteristics are listed in Table III. It is able to supply power to electric utility systems with extremely unbalanced loads up to 13.3 kVA rated power per phase, as well highly distorted loads as single-phase diode rectifier and very low power factor utilities as induction motors at starting operation.

As already mentioned in the simulations Section, the selected modulation scheme is the Carrier based PWM with triangular third harmonic injection.

The test bench as in Fig. 20 has been set up in order to carry out the experimental tests. Three different load conditions have been considered: three-phase non-linear

load for harmonic content evaluation, single-phase linear load for unbalanced load as well for load transient conditions, three-phase loads with variable power factor.

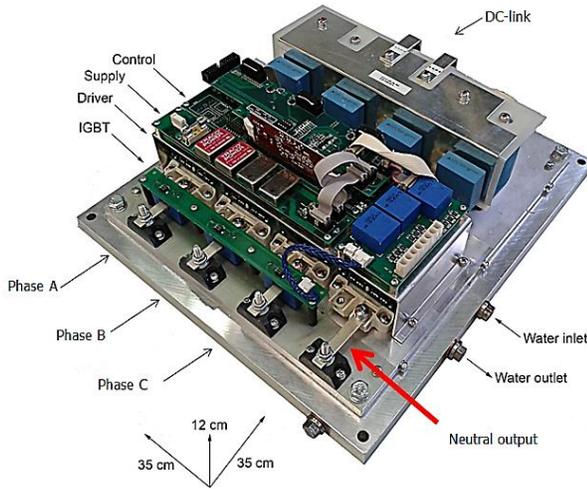


Fig. 19. 3-Phase 4-Leg Inverter prototype.

TABLE III
3-PHASE 4-LEG INVERTER PROTOTYPE MAIN CHARACTERISTICS

Rated Power	40 kVA
Line-to-Line Voltage	400±10% V
Switching Frequency	12 kHz
Efficiency @ rated power (output filter included)	0.97
Power Modules	Semikron - SEMIX303GB12Vs
Snubber Capacitors	MKP 1.2 μF – 1000 Vdc
DC-Link Capacitors	MKP 450 μF – 900 Vdc
AC Output Inductors	800 μH
AC Output Main Capacitors	MKP 5 μF – 480 Vac
Control Board	TMS320F28335

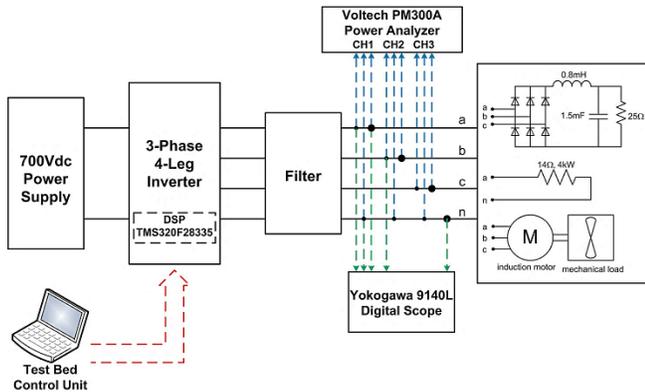


Fig. 20. Test Bench layout

A non-linear load has been arranged to test the system under extreme power quality conditions. As in the simulation tests, the non-linear load consists of a three-phase diode bridge rectifier configuration having 0.8 mH as input line inductance, 1.5 mF as output DC capacitance and 25 Ω as resistive DC load.

At steady state mode of operation, inverter line-to-line voltage, phase voltage and output current are illustrated in Fig. 21 when the described non-linear load is applied. Output voltage THD is about 1.7% when the current THD amounts to 48%.

With the proposed control solution, algorithms for compensation of switching devices Dead-Time (DT) are not required. In fact, in the experimental results shown in this work, the usual distortive effects introduced by DT are negligible even when the inverter DT compensation is disabled. DT introduces mainly fifth and seventh harmonics distortion, which is perfectly compensated by the repetitive control section, thus achieving a simplification in the whole control algorithm.

Dynamic performances of the investigated RR control have been preliminary tested through a load step test in extreme unbalanced conditions. A single-phase resistor having rated power of 4 kW at 230 Vrms is used as load step test. The resistive load is connected to the output of the inverter when the phase-to-neutral inverter voltage reaches its peak value. As it is shown in Fig. 22, both line-to-line and phase-to-neutral voltage drops are quite negligible due to the fast compensation provided by the implemented RR control.

As soon as the load is switched on, output voltages exhibit some distortion, due to the time required by the RC to compensate VSI filter voltage drops and DT effects. The amount of time required to reach the steady state THD value of 1% in the output voltage is around eight fundamental periods, which is equal to 160 ms considering EU frequency standard.

Experimental testing has been also carried out in case of reactive power loads and the relevant results are shown in Fig. 23. A three-phase induction motor is supplied at partial load operating conditions, in order to achieve a 0.6 power factor load condition.

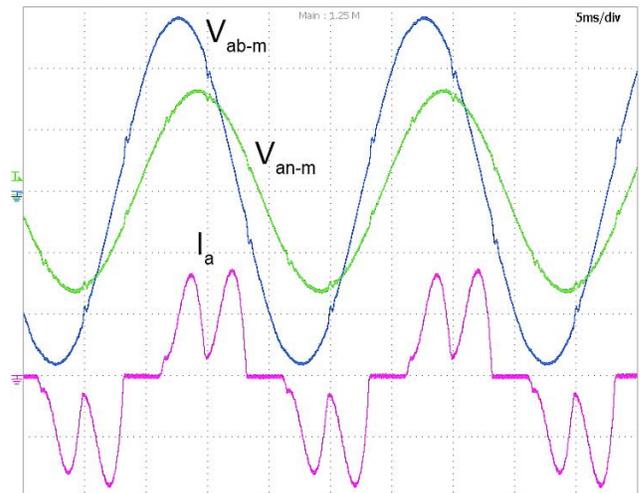


Fig. 21. Experimental results for a 3ph diode rectifier load. Voltage 200 V/div, current 20 A/div.

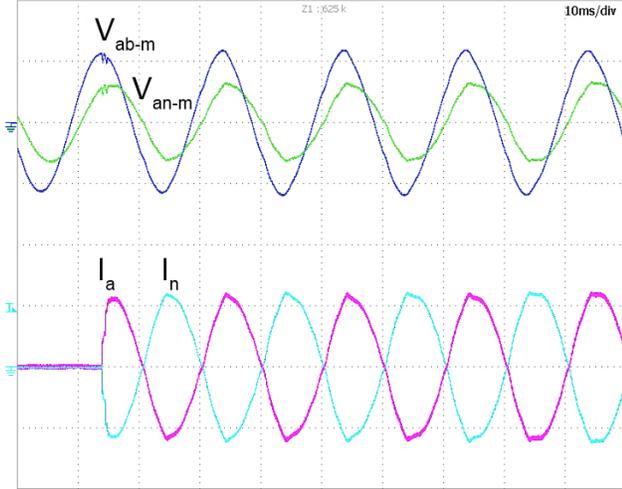


Fig. 22. Experimental results for a single phase resistive load step from no-load to 4 kW. Voltage 500 V/div, current 20 A/div.

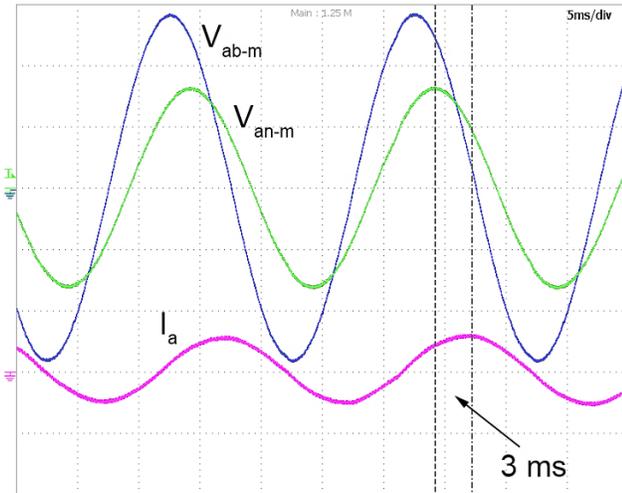


Fig. 23. Experimental results for three phase load with 0.6 power factor. Voltage 200 V/div, current 20 A/div.

VII. DISCUSSION ON DATA AND PROGRAM MEMORY REQUIREMENTS

Memory requirements for the proposed RR control versus both the multi resonant control configuration and conventional RC stabilized by a simple integrator are analyzed, compared and contrasted. A common implementation benchmark is to consider 32-bit single-precision floating-point arithmetic for all the topologies and per phase necessity. These analysis results are summarized in Table IV.

A single RSC discretized by trapezoidal approximation with frequency pre-warp requires 11 variables to be stored in data memory as well 5 multiplications, 4 additions/subtractions and 4 assignments.

RC needs two delay lines with length dependent on the ratio between the sampling and the fundamental frequencies. According to the value of 12 kHz for the

sampling frequency and the fundamental frequency of 50 Hz, RC data memory requests are 240 and 236 delay lines with 2 single length temporary variables and a pair of 16 bits variables for indexing. Moreover, 2 multiplications, 2 sum operations, 2 increments, 2 comparisons and 6 assignments need to be stored in the program memory.

Considering the Multi-RSC up to the 50th harmonic, that is usually required to be compliant with EMC 61000 Class-1 standard, data memory occupancy is around 2200 bytes per phase. On the contrary, the investigated RR needs around 1970 bytes per phase; this means almost 11 % reduction in data memory occupancy. When program memory requirements are considered, RR needs a total 27 operations to be executed, with different weights in memory. However, the considered Multi-RSC has to store 650 operating codes in the program memory. According to that, the proposed combined RR algorithm execution time is substantially lower than a Multi-RSC, being executed through fewer operations with respect to the RSC, in which the operations must be sequentially executed.

Data and program memory requirements of a classical Repetitive Control stabilized by a simple Integrator (RI) have been counted. The proposed RR structure do not significantly affect the memory and computational burden requirements of the conventional RC configuration, as reported in Table IV. The small difference in memory requirements is mainly related to the z-domain implementation of the Integrator in RI with respect to the Resonant Controller of the RR strategy, which requires a little bit more instructions being a second order system.

TABLE IV
ALGORITHM MEMORY REQUIREMENTS (PER PHASE)

	Data memory [bytes]	Program memory [instructions]
RR	≈1970	≈27
Multi-RSC (50 th)	≈2200	≈650
RI	≈1967	≈25

CONCLUSIONS

A combined Repetitive-Resonant (RR) control algorithm for power electronics systems is investigated in this research work, using as validation benchmark a 40 kVA stand-alone 4-leg AC power supply unit. The proposed control structure is validated with both simulation and experimental tests. Results show the excellent performance obtained by this control solution, in terms of output power quality, control dynamics, computational requirements and robustness. The use of a resonant filter tuned at the system fundamental frequency provides both system stabilization and excellent fundamental component tracking; a plug-in repetitive controller compensates for the harmonics in the controlled variable and allows the gain of the resonant filter to be

increased for better compensation, without affecting system stability and robustness. The system performance has been tested in case of three different load conditions: 3-phase non-linear load for harmonic content evaluation, 1-phase linear load for unbalanced load as well for load transient conditions, 3-phase loads with variable power factor.

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